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Jurgen Vollrath
588 Sutter Street #531
San Francisco, CA 94102
09/07/2005 SFELEKE2 00000042 140448 10716276

01 FC:1501 1400.00 OP
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JURGEN VOLLRATH (Depositor's name)
<i>J. Vollrath</i> (Signature)
9/3/05 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/716,276	11/17/2003	Peter J. Hopper	P05731	6046

TITLE OF INVENTION: METHOD OF ETCHING A LATERAL TRENCH UNDER AN EXTRINSIC BASE AND IMPROVED BIPOLAR TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	09/13/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
KANG, DONGHEE	2811	438-318000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 *Jurgen Vollrath*
2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

*National Semiconductor Corporation, 2900 Semiconductor Dr., MS D3-579
Santa Clara, CA*

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

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Authorized Signature *J. Vollrath*

Date **8/31/05**

Typed or printed name **JURGEN VOLLRATH**

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